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 **PALM INTRANET**

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US 20050052912 A1	US PG PUB	20050310	9	Circuit and system for addressing memory modules	365/202		Cogdill, Mike et al.
US 20050062554 A1	US PG PUB	20050324	8	Termination stub system and method	333/22R		Cogdill, Mike et al.
US 20050085007 A1	US PG PUB	20050421	7	Joining material stencil and method of use	438/106		Vu, Chuong et al.
US 20050086418 A1	US PG PUB	20050421	10	Circuit and system for accessing memory modules	711/100		Cogdill, Mike et al.
US 20050094425 A1	US PG PUB	20050505	11	Double-high memory system compatible with termination schemes for single-high memory systems	365/63		Cogdill, Mike et al.
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